PLECS WORKSHOP

Real-Time Simulation Using the PLECS RT Box
Plexim GmbH, 20. October 2027

08:30	Registration and Installation of Necessary Features
09:00	Overview and Introduction to RT Box Workflow using PLECS ▶I PLECS overview ▶I From PLECS offline models to RCP and HIL ▶I Code Generation
09:45	RT Box Introductory Exercise ▶I PLECS RT Box features Exercise: Introductory exercise using I/O ports
10:30	RT Box Library Overview
10:45	Break
11:00	Real-time Simulation of a Voltage Source Inverter (VSI) ▶I PLECS model creation using the target blocks library ▶I Deployment on the RT Box Exercise: Voltage Source Inverter (VSI)
12:00	Lunch
13:00	Solver Engine ▶I RT Box architecture ▶I Sub-cycle averaging concept ▶I FPGA simulation using FlexArray solver ▶I FPGA simulation using Nanostep® solver Exercise: Solver engine
15:00	Break
15:15	Solver Engine Exercise: Solver engine
16:00	RT Box Hardware-in-the-Loop (HIL) Application Demo, Q&A
16:30	End of Workshop
Contact	Plexim GmbH, +41 44 533 51 00, info@plexim.com
Location	Plexim GmbH, 3rd Floor, Zeppelin Wing, Technoparkstrasse 1, 8005 Zurich, Switzerland
Note	This workshop addresses to anyone who already has experience with the simulation software PLECS.

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