

PLECS WORKSHOP

Process-in-the-Loop Simulation and Advanced Modeling of Power Electronic Systems
Shanghai Jiao Tong University, May 31, 2016

08:30-09:00	Registration
09:00-10:30	Introduction to PLECS <ul style="list-style-type: none">✓ General use of PLECS Blockset & PLECS Standalone✓ Instantaneous switching✓ Variable and fixed step operation Exercise: Model a switched-mode power supply
10:30-10:45	Break
10:45-12:00	Introduction to Thermal, Mechanical, Magnetic Modeling & Simulation <ul style="list-style-type: none">✓ Switching & conduction loss descriptions✓ Combined electrical-thermal simulation✓ Permeance Capacitance Analogy Model Exercise: Thermal modeling of a buck converter
12:00-13:00	Lunch

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13:00-14:30	Principle of a Processor-in-the-Loop simulation <ul style="list-style-type: none">✓ Integration of PIL✓ Integration of the PIL framework into an embedded project✓ Setting up PLECS for PIL Exercise: PIL simulation for a buck converter
14:30-14:45	Break
14:45-16:00	Hardware-in-the-loop simulation RT-Box Demo
16:00	End of Workshop
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Venue	Room 214, State Energy Smart Grid R&D Center
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