

PLECS WORKSHOP

Processor-in-the-Loop Simulation and Advanced Modeling of Power Electronic Systems Day 2
Aalborg University, October 26, 2016

08:30	Registration
09:00	Analysis Tools (New Multitone Analysis) <ul style="list-style-type: none">▶ Steady state analysis▶ AC sweep and impulse response analysis Exercise: Control design of a flyback converter using PLECS
10:15	Break
10:30	Principle of a Processor-in-the-Loop simulation <ul style="list-style-type: none">▶ Integration of PIL▶ Framework into an embedded project▶ Setting up PLECS for PIL Exercise: PIL simulation for a BLDC motor control
12:00	Lunch
13:00	Processor-in-the-Loop simulation (continuation) Exercise: PIL simulation for a BLDC motor control
14:00	Break
14:15	Code Integration via C-Script and DLL block <ul style="list-style-type: none">▶ Solver operation▶ Sample time settings▶ DLL block Exercise: Efficient PWM generation using the PLECS C-Script block Q&A
17:00	End of day
Contact	Plexim GmbH, +41 44 533 51 00, info@plexim.com
Location	Aalborg University, Room 3.115, Pontoppidanstraede 105, 9220 Aalborg
Note	This workshop on October 26 addresses to anyone who already has experience with the simulation software PLECS. There will be another workshop (basic) on October 25 in the same location.