PLECS WORKSHOP

Processor-in-the-Loop Simulation and Advanced Modeling of Power Electronic Systems
Technical University of Denmark, April 14, 2016

08:30	**Registration**
09:00	**Analysis Tools including Multitone Analysis** ▶▎ Steady state analysis ▶▎ AC sweep and impulse response analysis Exercise: Control design of a flyback converter using PLECS
10:15	**Break**
10:30	**Principle of a Processor-in-the-Loop Simulation** ▶▎ Integration of PIL ▶▎ Framework into an embedded project ▶▎ Setting up PLECS for PIL Exercise: PIL simulation for a BLDC motor control
11:45	**Lunch**
12:45	**Processor-in-the-Loop Simulation (Continuation)** Exercise: PIL simulation for a BLDC motor control
14:00	**Break**
14:15	**Code Integration via C-Script and DLL block** ▶▎ Solver operation ▶▎ Sample time settings ▶▎ DLL block Exercise: Efficient PWM generation using the PLECS C-Script block
16:30	**Q&A**
17:00	**End of day**
Contact	**Plexim GmbH, +41 44 533 51 00, info@plexim.com**
Location	**Technical University of Denmark, Flex Lab. 241, DTU Building 325, 2nd Floor, 2800 Kgs. Lyngby, Denmark**