

PLECS WORKSHOP

Advanced Modeling and Simulation of Power Electronic Systems
Università La Sapienza, 28 October 2026

08:30	Registration
09:00	Introduction to PLECS ▶ General use of PLECS Blockset and PLECS Standalone ▶ Instantaneous switching ▶ Variable and fixed-step operation Exercise: Modeling a switched-mode power supply
10:30	Solver Settings ▶ Definition of stiff and non-stiff systems ▶ Explicit and non-explicit solvers ▶ Stability domains ▶ Accuracy considerations, step size control ▶ Proper handling of discontinuities, zero-crossing detection
11:30	Break
12:00	Introduction to Thermal Simulation ▶ Switching & conduction loss descriptions ▶ Combined electrical-thermal simulation ▶ Determining loss values from data sheets Exercise: Thermal modeling of a buck converter
14:00	Bringing Simulation and Real Hardware Together (Code Generation Workflow in PLECS) ▶ Automatic code generation for microcontrollers ▶ Plant code generation for the RT Box ▶ Nanostep®, FlexArray and CPU solver engines ▶ Real-time simulation with 4ns step size for MHz switching Demonstration: HIL testbench with microcontroller and RT Box
16:00	Q&A
16:15	End of day
Contact	Plexim GmbH, +41 44 533 51 00, info@plexim.com
Location	Sapienza – Università Di Roma Via Eudossiana, 18 Sala degli Affreschi 9-13 (08:30 – 14:00) Sala Consiliare 14-16 (14:00 – 16:00) 00184 Roma Italy