

# PLECS WORKSHOP

Advanced Modeling and Simulation of Power Electronic Systems

University of Nottingham, 16<sup>th</sup> April 2026

8:30	Registration
09:00	<b>The PLECS Toolchain</b> <ul style="list-style-type: none"><li>▶ PLECS, PLECS Coder, and RT Box</li><li>▶ General use of PLECS, modeling vs. simulation</li><li>▶ Instantaneous switching, variable and fixed-step operation</li><li>▶ Simulation domains and system-level simulation</li><li>▶ Tools: steady-state and small-signal analysis, state machine, simulation scripting</li></ul> Exercise: Simulation of a flyback converter
10:40	Break
11:00	<b>Solver Settings</b> <ul style="list-style-type: none"><li>▶ Definition of stiff and non-stiff systems</li><li>▶ Explicit and non-explicit solvers</li><li>▶ Stability domains</li><li>▶ Accuracy considerations, step size control</li><li>▶ Proper handling of discontinuities, zero-crossing detection</li></ul>
12:00	Lunch
13:00	<b>PLECS Spice</b> <ul style="list-style-type: none"><li>▶ Seamless integration of full-fledged SPICE solver inside PLECS Standalone</li><li>▶ Coupling the PLECS controls domain with a SPICE power stage using variable-step solver</li></ul> Exercise: Flyback converter with SPICE device models
14:40	Break
15:00	<b>Bringing Simulation and Real Hardware Together</b> <b>(Code Generation Workflow in PLECS)</b> <ul style="list-style-type: none"><li>▶ Automatic code generation for microcontrollers</li><li>▶ Plant code generation for the RT Box</li><li>▶ Nanostep®, FlexArray and CPU solver engines</li><li>▶ Real-time simulation with 4ns step size for MHz switching</li></ul> Demonstration: HIL testbench with microcontroller and RT Box
16:00	Q&A
16:15	End of day
Contact	Plexim GmbH, +41 44 533 51 00, <a href="mailto:info@plexim.com">info@plexim.com</a>
Location	Pope Building (Building 27), Room B01, B-Floor University Park, Nottingham NG7 2RD United Kingdom