

PLECS

*DEMO MODEL*

## Buck Converter with Thermal Model

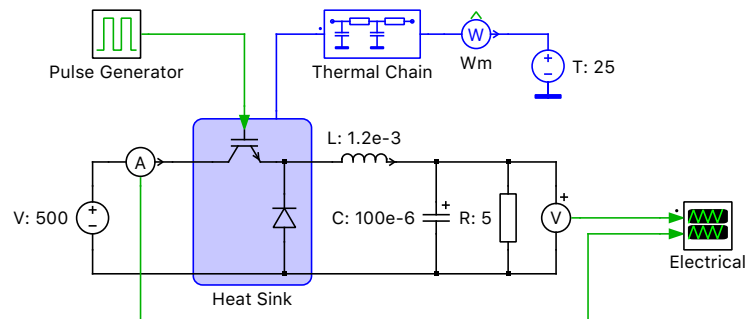
Last updated in PLECS 4.3.1

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# 1 Overview

This demonstration shows a simple unregulated buck converter including a basic thermal model. It serves as an example for using thermal descriptions, the heat sink and the thermal chain.



**Figure 1: Buck converter**

## 2 Model

### 2.1 Electrical model

This schematic shows a simple buck converter using an IGBT. The circuit is clocked with a fixed frequency of 10 kHz.

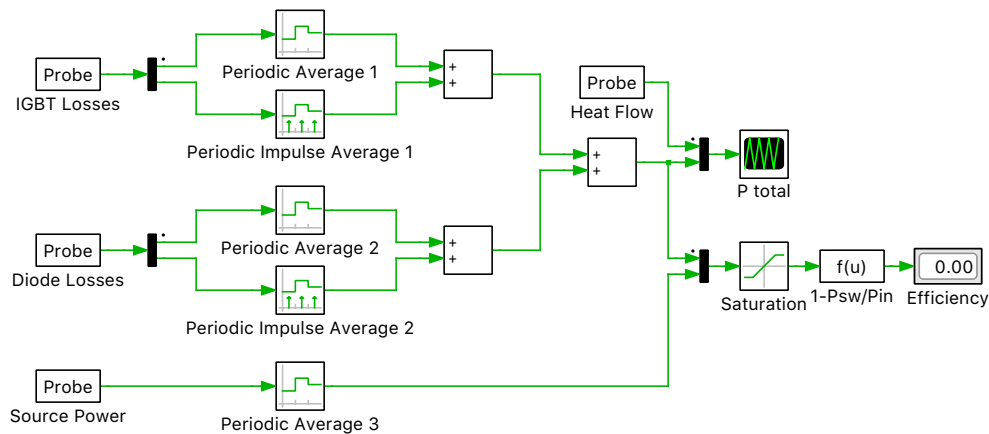
### 2.2 Thermal model

An example thermal description is assigned to both the IGBT and the free wheeling diode. These descriptions can be viewed and edited by double-clicking on the component and selecting **Edit...** from the drop-down menu of the **Thermal description** parameter. The thermal parameters were obtained from measurements of commonly available semiconductors.

The two semiconductors can be considered to be packaged together as a chopper module. In this example the heat sink represents not the actual heat sink in the circuit, but a common surface that the two devices are connected to, and should be considered as the case. A Thermal Chain component is used to model the thermal transitions of the case of the semiconductors to the actual heat sink and from the heat sink to the ambient temperature of the environment. Note that hierarchical modeling for additional thermal layers, such as to include not only the case, but also the heat sink, is possible in PLECS. An example of this concept is shown in the demo model “Boost Converter with PFC and Thermal Model”.

The thermal descriptions for the IGBT and diode are stored in a private thermal library in the directory `/buck_converter_with_thermal_model_plecs`.

The instantaneous switching and conduction losses of semiconductors can be calculated easily. In order to obtain averaged loss values, filtering blocks should be used to condition the instantaneous loss signals, which are accessed by probing any semiconductor. The Periodic Average component can calculate the average conduction losses while the Periodic Impulse Average component can calculate the average switching losses. The total losses of all semiconductors can also be summed together.



**Figure 2: Thermal losses**

## 3 Simulation

The first Scope shows the output voltage and the source current. The second Scope shows the temperatures of the semiconductor junctions and case of the combined chopper package. The electrical values reach equilibrium very early on in the simulation while the temperature takes much longer to approach steady-state conditions.

### 3.1 Steady-state operation

The steady-state operating temperatures can be calculated with the Steady-State Analysis that is pre-configured in the model. For PLECS Standalone or Blockset (in Simulink) execute the following steps:

- *Standalone:* From the **Simulation** menu choose **Analysis tools...** This opens a dialog, in which the analysis has already been pre-configured. To start the analysis, click on the **Start analysis** button. You can display the progress of the analysis by clicking on the **Show log** button.
- *Blockset:* Double-click on the Steady-State Analysis block to open the dialog and click on the **Start analysis** button. The progress of the analysis is displayed in the MATLAB Command Window.

When the analysis has finished a simulation of five steady-state cycles is displayed for all waveforms preconfigured in the Scope.

### 3.2 Loss and efficiency calculation

The total average losses of the diode and IGBT are calculated and compared with the heat flow in the thermal circuit. Note that it takes several seconds for the heat sink to warm up, while the loss probes are connected directly to the source components. In this case the converter efficiency is calculated using the source power and total switch losses, but the heat sink can also capture ohmic losses from resistors.

## 4 Conclusion

This model uses blocks from the PLECS thermal domain to calculate the switching and conduction losses of an IGBT and a diode as well as model the thermal impedance network between device junction and ambient temperature. Control domain blocks are used to extract the same loss information without the use of the thermal domain, and to calculate the circuit efficiency.

## Revision History:

PLECS 4.3.1      First release

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